

(19) World Intellectual Property Organization
International Bureau(43) International Publication Date
14 December 2000 (14.12.2000)

PCT

(10) International Publication Number
WO 00/75881 A1(51) International Patent Classification⁷: G07F 7/10

(21) International Application Number: PCT/EP00/05017

(22) International Filing Date: 31 May 2000 (31.05.2000)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
99870113.0 4 June 1999 (04.06.1999) EP

(71) Applicant and

(72) Inventor: D'UDEKEM D'ACQZ, Xavier, Guy, Bernard
[BE/BE]; Avenue de Villequier 14, B-1410 Waterloo (BE).

(72) Inventor; and

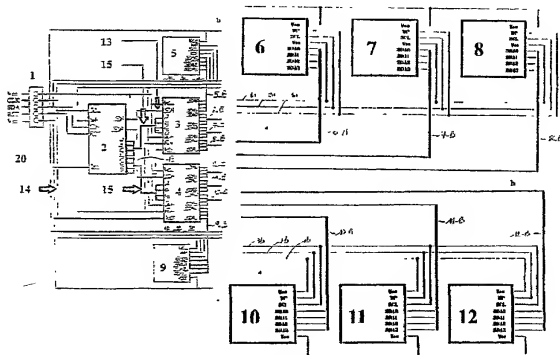
(75) Inventor/Applicant (for US only): DELAHAYE, Serge,
Alphonse, Marcel, Romain [FR/FR]; 658 Avenue J.F.
Kennedy, F-84200 Carpentras (FR).(74) Agents: BIRD, Ariane et al.; Bird Goën & Co, Vilvoord-
sebaan 92, B-3020 Winksele (BE).(81) Designated States (national): AE, AG, AL, AM, AT, AU,
AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE,
DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU,
ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS,
LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ,
PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT,
TZ, UA, UG, US, UZ, VN, YU, ZA, ZW.(84) Designated States (regional): ARIPO patent (GH, GM,
KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian
patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European
patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE,
IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG,
CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

— With international search report.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: CARD MEMORY APPARATUS



(57) Abstract: The present invention includes a card memory device (20) comprising a microprocessor (2), a plurality of memory units (5-12) for storage of digital data, the memory units (5-12) being embedded in the card memory device (20). A selecting device (3, 4) is provided for selecting one of the memory units (5-12) and for routing address information and data to the selected memory unit (5-12), both the microprocessor (2) and the selecting device (3, 4) being embedded in the card memory device (20). The card memory device integrates components into a thin flexible memory card with surface contacts (1) so that the components can cooperate as a compact unit providing environmental sealing and secure access to several Mbytes of digital data. A specially designed set (1) of contacts (21-25) are also described which reduce the risk of electrostatic discharge. The card memory device (20) may be used for secure control of a personal computer.